Electronic Acknowledgement Receipt				
EFS ID:	4616130			
Application Number:	10016449			
International Application Number:				
Confirmation Number:	5286			
Title of invention:	Method and system for use of a field programmable gate array (FPGA) function within an application specific integrated circuit (ASIC) to enable creation of a debugger client within the ASIC			
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Filer Authorized By:	Joseph A. Sawyer			
Attorney Docket Number:	RPS920010127US1			
Receipt Date:	15-JAN-2009			
Filing Date:	10-DEC-2001			
Time Stamp:	01:49:30			
Application Type:	Utility under 35 USC 111(a)			

Payment information:

Information:

Submitted with Payment		no	no			
File Listing:						
Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)	
1 Warnings:	Amendment/Req. Reconsideration-After Non-Final Reject	RPS920010127US1_2280P_Am endAsFiled_2009-01-13.pdf	36146	no	7	
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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.